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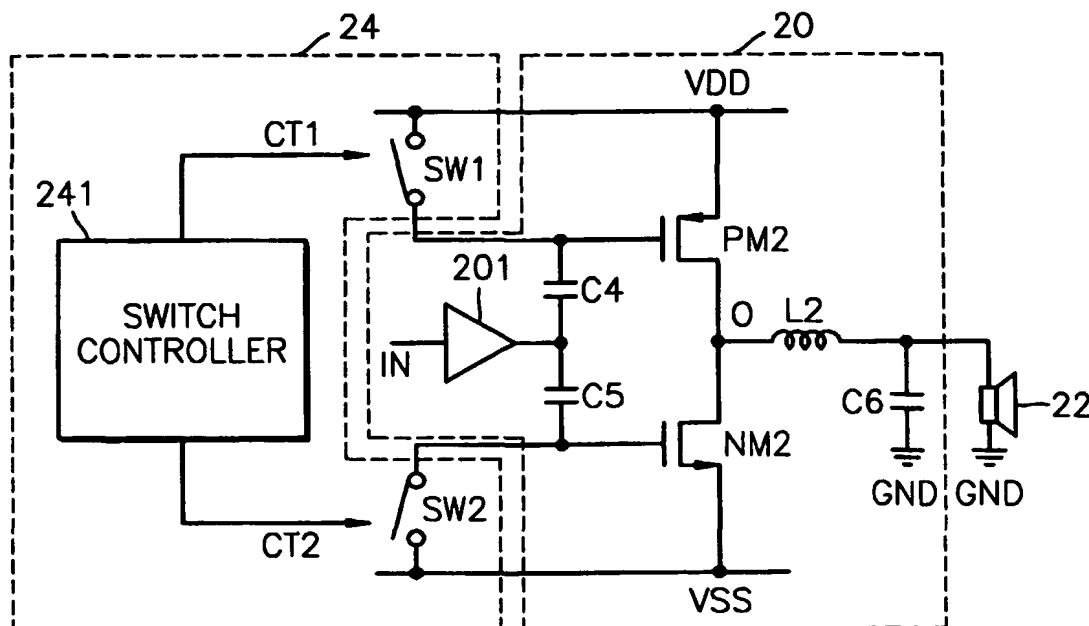
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[Continued on next page]

(54) Title: CIRCUIT AND METHOD FOR ELIMINATING POP NOISE IN DIGITAL AUDIO AMPLIFIER USING DUAL POWER SUPPLY



(57) Abstract: Provided are a circuit for and a method of eliminating pop noise in a digital audio amplifier using dual power supply, which are simple, drop in price, and can be readily implemented in a semiconductor chip. According to an existing technique, pop noise is eliminated using a relay. However, in the present circuit and method, pop noise is eliminated using the small number of discrete electronic devices. The circuit for eliminating pop noise controls a voltage at a gate of a power switch, i.e., a power MOS transistor, when power supply voltages are applied and the application of power supply voltages is stopped.



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INTERNATIONAL SEARCH REPORT

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A. CLASSIFICATION OF SUBJECT MATTER

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B. FIELDS SEARCHED

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Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Korean Patents and applications for inventions since 1975

Korean Utility models and applications for Utility models since 1975

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

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C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 4370623 A(Kabushiki Kaisha Nagasawa) Jan. 25, 1983 see the whole document	1 ~ 9
A	US 5070308 A(Gyula Padi) Dec. 3, 1991 see the whole document	1 ~ 9
A	US 5361041 A(Unitrode Coporation) Nov. 1, 1994 see the whole document	1 ~ 9
A	US 5537081 A(National Semiconductor Corporation) Jul. 16, 1996 see the whole document	1 ~ 9



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